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REV: V10

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DEPARTMENT: DEPARTMENT = MINGTECH-IN

DESIGNER: DESIGNER = xingjiuge

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DATE : Page Modify Date = Thursday, August 2, 2018

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Revision History

REV	DESCRIPTION
A	EVT1.2 release. The detailed change refer to item 29 to 34 on page <Change list>.

Board Change History

SCHEMATIC MCN	HW VERSION	REVISION	DESCRIPTION OF CHANGE
LD20-PF494-11	EVT1.0	A	Preliminary release for early reference.
		B	The detailed change refer to item 1 to 8 on page <Change list>.
		C	The detailed change refer to item 9 to 18 on page <Change list>.
LD20-PF494-12	EVT1.1	A	The detailed change refer to item 19 to 26 on page <Change list>.
		B	The detailed change refer to item 27 to 28 on page <Change list>.
LD20-PF494-13	EVT1.2	A	The detailed change refer to item 29 to 34 on page <Change list>.

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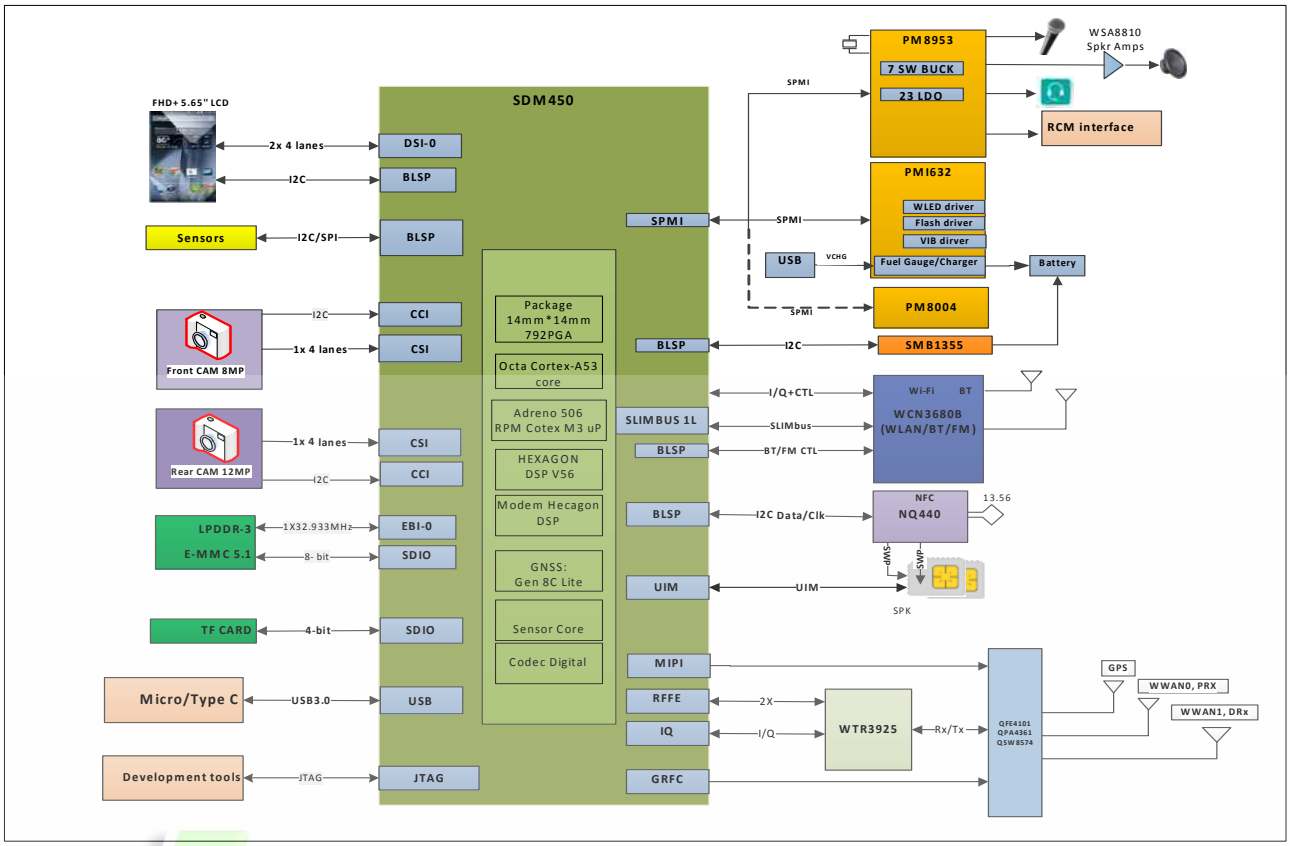

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SDM450 GPIO Configuration For QRD450+PMI632

GPIO_0		GPIO_41	DTV_EN	GPIO_82	FM_DATA
GPIO_1		GPIO_42	ACC_GYRO_INT1	GPIO_83	BT_CTL
GPIO_2	PD_I2C_SDA	GPIO_43	ALSP_INT	GPIO_84	BT_DATA
GPIO_3	PD_I2C_SCL	GPIO_44		GPIO_85	KEY_VOL_UP_N
GPIO_4	UART_TX_2	GPIO_45	ACC_GYRO_INT2	GPIO_86	SAR_PS_INT
GPIO_5	UART_RX_2	GPIO_46		GPIO_87	ANT_MATCH_H
GPIO_6	SAR_SPA_I2C_SDA	GPIO_47		GPIO_88	SPKPA_I2S_DATAI
GPIO_7	SAR_SPA_I2C_SCL	GPIO_48	FP_INT	GPIO_89	DTV_RST
GPIO_8	FP_RST_N	GPIO_49	UIM_BATT_ALARM	GPIO_90	
GPIO_9	CC_INT	GPIO_50	CAM_DVDD_EN	GPIO_91	SPKPA_I2S_BCK
GPIO_10	TP_I2C_SDA	GPIO_51	UIM1_DATA	GPIO_92	SPKPA_I2S_LRCK
GPIO_11	TP_I2C_SCL	GPIO_52	UIM1_CLK	GPIO_93	SPKPA_I2S_DATAO
GPIO_12	FP_LDOEN_3V3	GPIO_53	UIM1_RESET	GPIO_94	AUDIO_PA_RST
GPIO_13		GPIO_54	UIM1_PRESENT	GPIO_95	TP_LDO_1V8_EN
GPIO_14	SENSOR_I2C_SDA	GPIO_55	UIM2_DATA	GPIO_96	FP_SPI_MOSI
GPIO_15	SENSOR_I2C_SCL	GPIO_56	UIM2_CLK	GPIO_97	FP_SPI_MISO
GPIO_16	NFC_DISABLE	GPIO_57	UIM2_RESET	GPIO_98	FP_SPI_CS
GPIO_17	NFC_IRQ	GPIO_58	UIM2_PRESENT	GPIO_99	FP_SPI_CLK
GPIO_18	NFC_LCD_I2C_SDA	GPIO_59	SPK/REC_SW_EN	GPIO_100	
GPIO_19	NFC_LCD_I2C_SCL	GPIO_60		GPIO_101	GRFC1_SEL
GPIO_20	SENSOR_SPI_MOSI	GPIO_61	LCD_RST	GPIO_102	GRFC2_SEL
GPIO_21	SENSOR_SPI_MISO	GPIO_62		GPIO_103	GRFC3_SEL
GPIO_22	SENSOR_SPI_CS0_N	GPIO_63	SPKPA_INT	GPIO_104	
GPIO_23	SENSOR_SPI_CLK	GPIO_64	TP_RST	GPIO_105	
GPIO_24	LCD_TE	GPIO_65	TP_IN	GPIO_106	WDOG_DISABLE
GPIO_25	LCD_ID1	GPIO_66	LCD_ID0	GPIO_107	GRFC7_SEL
GPIO_26	CAM_MCLK0	GPIO_67	CDC_PDM_RX0_COMP	GPIO_108	GRFC8_SEL
GPIO_27	CAM_MCLK1	GPIO_68	CDC_PDM_RX1_COMP	GPIO_109	
GPIO_28		GPIO_69	CDC_PDM_CLK	GPIO_110	GRFC10_SEL
GPIO_29	CAM_I2C_SDA0	GPIO_70	CDC_PDM_SYNC	GPIO_111	GRFC11_SEL
GPIO_30	CAM_I2C_SCL0	GPIO_71	CDC_PDM_TX	GPIO_112	GRFC12_SEL
GPIO_31	CAM_I2C_SDA1	GPIO_72	CDC_PDM_RX0	GPIO_113	
GPIO_32	CAM_I2C_SCL1	GPIO_73	CDC_PDM_RX1	GPIO_114	GRFC14_SEL
GPIO_33		GPIO_74	CDC_PDM_RX2	GPIO_115	GRFC15_SEL
GPIO_34	FLASH_STROBE_NOW	GPIO_75	BT_SSBI	GPIO_116	EXT_GPS_LNA_EN
GPIO_35	CAM_AVDD_EN	GPIO_76	WL_CMD_DATA_2	GPIO_117	CH0_GSM_TX_PHASE_D0
GPIO_36	FP_LDOEN_1V8	GPIO_77	WL_CMD_DATA_1	GPIO_118	RFFE1_CLK
GPIO_37	FORCED_USB_BOOT	GPIO_78	WL_CMD_DATA_0	GPIO_119	RFFE1_DATA
GPIO_38	GPIO38_DTV_EINT	GPIO_79	WL_CMD_SET	GPIO_120	RFFE2_CLK
GPIO_39	REAR_MCAM_PWDN	GPIO_80	WL_CMD_CLK	GPIO_121	RFFE2_DATA
GPIO_40		GPIO_81	FM_SSBI	GPIO_122	

GPIO_123		GPIO_134	BOARD_ID2
GPIO_124	RFFE5_CLK	GPIO_135	DTV_SPI_CLK
GPIO_125	RFFE5_DATA	GPIO_136	DTV_SPI_CS_N
GPIO_126		GPIO_137	DTV_SPI_MOSI
GPIO_127		GPIO_138	DTV_SPI_MISO
GPIO_128	SPK/REC_SW_IN	GPIO_139	USB_SS_SEL
GPIO_129		GPIO_140	
GPIO_130	FRONT_CAM_RST	GPIO_141	
GPIO_131	BOARD_ID1		
GPIO_132			
GPIO_133	SDCARD_DET_N		

PMI632 GPIO/MPP Configuration

GPIO_1	TYPE_C_CON_THERMAL	GPIO_5	
GPIO_2		GPIO_6	NEBULA_PWM
GPIO_3	QUITE_THERMAL	GPIO_7	
GPIO_4	FLASH_STROBE_NOW	GPIO_8	

PM8953 GPIO/MPP Configuration

GPIO_1		MPP_1	VDD_PX_BIAS_MPP_1
GPIO_2	NFC_CLK_REQ	MPP_2	CHG_THERM
GPIO_3	UIM_BATT_ALARM	MPP_3	VREF_DAC_MPP_3
GPIO_4	WLED_EN	MPP_4	MSM_THERM
GPIO_5			
GPIO_6	VIB_RST_N		
GPIO_7			
GPIO_8	LCD_BL_PWM		

GPIO TABLE

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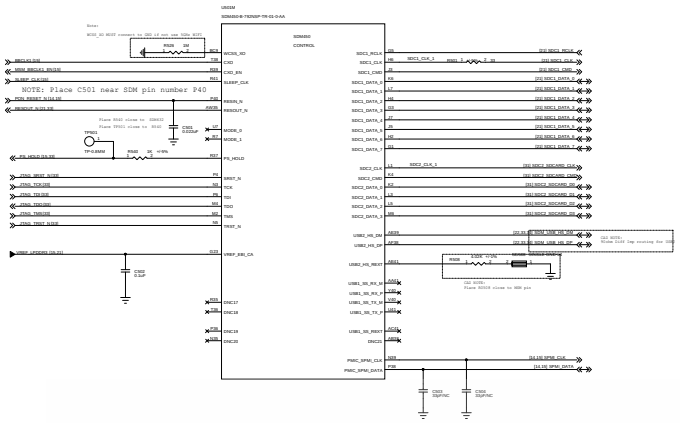

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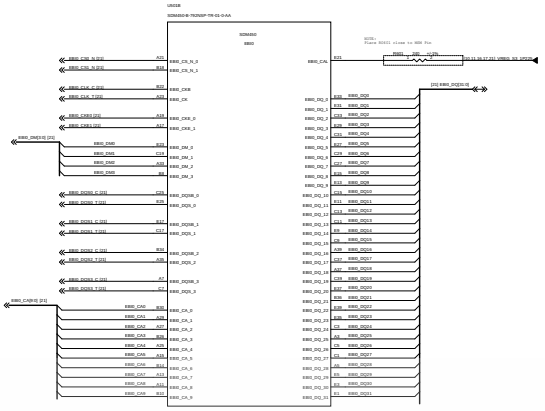

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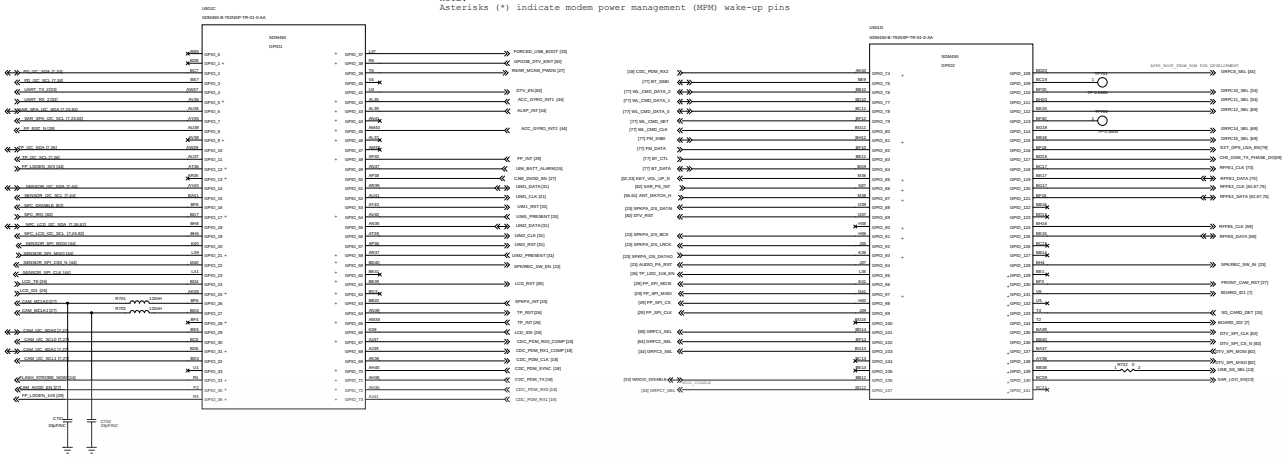
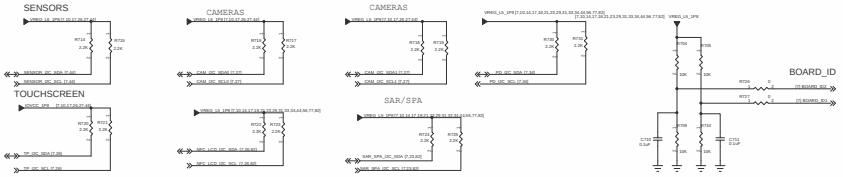
NOTE:
Asterisks (*) indicate modem power management (MPM) wake-up pins



Change pull up resistors is 2.2K

I2C PULL-UP RESISTORS



GPIO_77	APPS_BOOT_MODE
GPIO_108	BOOT_CONFIG0
GPIO_109	APPS_BOOT_FROM_ROM
GPIO_113	BOOT_CONFIG1
GPIO_114	BOOT_CONFIG2
GPIO_115	BOOT_CONFIG3

BOOT_CONFIG[3:1]	BOOT_CONFIG
0x000	EDC2 -> EDC2 -> USB2.0
0x001	EDC2 -> EDC-> USB2.0
0x010	EDC-> USB2.0
0x011	USB2.0

Default Boot Config (0x000) is EDC2(eMMC)

NOTE:
Ensure SW sets these GPIOs (Sensor, CTP and Camera I2C bus) to inout pull down when the peripherals are powered off to eliminate leakage.

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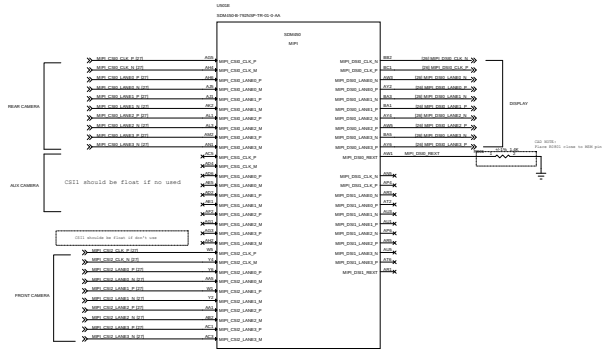

CSI Pin Name	CSI DPHY 4-lane	CSI DPHY 2+1 Mode	CSI CPHY 3Phase Mode
CSI0_3PHASE_PIN0	CSI0_CLKP	CSI0_2LANE_CLKP	NC
CSI0_3PHASE_PIN1	CSI0_CLKN	CSI0_2LANE_CLKN	CSI0_TRI0_A
CSI0_3PHASE_PIN2	CSI0_DP0	CSI0_2LANE_DP0	CSI0_TRI0_B
CSI0_3PHASE_PIN3	CSI0_DN0	CSI0_2LANE_DN0	CSI0_TRI0_C
CSI0_3PHASE_PIN4	CSI0_DP1	CSI0_2LANE_DP1	CSI0_TRI1_A
CSI0_3PHASE_PIN5	CSI0_DN1	CSI0_2LANE_DN1	CSI0_TRI1_B
CSI0_3PHASE_PIN6	CSI0_DP2	CSI0_1LANE_DP0	CSI0_TRI1_C
CSI0_3PHASE_PIN7	CSI0_DN2	CSI0_1LANE_DN0	CSI0_TRI2_A
CSI0_3PHASE_PIN8	CSI0_DP3	CSI0_1LANE_CLKP	CSI0_TRI2_B
CSI0_3PHASE_PIN9	CSI0_DN3	CSI0_1LANE_CLKN	CSI0_TRI2_C
CSI1_3PHASE_PIN0	CSI1_CLKP	CSI1_2LANE_CLKP	NC
CSI1_3PHASE_PIN1	CSI1_CLKN	CSI1_2LANE_CLKN	CSI1_TRI0_A
CSI1_3PHASE_PIN2	CSI1_DP0	CSI1_2LANE_DP0	CSI1_TRI0_B
CSI1_3PHASE_PIN3	CSI1_DN0	CSI1_2LANE_DN0	CSI1_TRI0_C
CSI1_3PHASE_PIN4	CSI1_DP1	CSI1_2LANE_DP1	CSI1_TRI1_A
CSI1_3PHASE_PIN5	CSI1_DN1	CSI1_2LANE_DN1	CSI1_TRI1_B
CSI1_3PHASE_PIN6	CSI1_DP2	CSI1_1LANE_DP0	CSI1_TRI1_C
CSI1_3PHASE_PIN7	CSI1_DN2	CSI1_1LANE_DN0	CSI1_TRI2_A
CSI1_3PHASE_PIN8	CSI1_DP3	CSI1_1LANE_CLKP	CSI1_TRI2_B
CSI1_3PHASE_PIN9	CSI1_DN3	CSI1_1LANE_CLKN	CSI1_TRI2_C
CSI2_3PHASE_PIN0	CSI2_CLKP	CSI2_2LANE_CLKP	NC
CSI2_3PHASE_PIN1	CSI2_CLKN	CSI2_2LANE_CLKN	CSI2_TRI0_A
CSI2_3PHASE_PIN2	CSI2_DP0	CSI2_2LANE_DP0	CSI2_TRI0_B
CSI2_3PHASE_PIN3	CSI2_DN0	CSI2_2LANE_DN0	CSI2_TRI0_C
CSI2_3PHASE_PIN4	CSI2_DP1	CSI2_2LANE_DP1	CSI2_TRI1_A
CSI2_3PHASE_PIN5	CSI2_DN1	CSI2_2LANE_DN1	CSI2_TRI1_B
CSI2_3PHASE_PIN6	CSI2_DP2	CSI2_1LANE_DP0	CSI2_TRI1_C
CSI2_3PHASE_PIN7	CSI2_DN2	CSI2_1LANE_DN0	CSI2_TRI2_A
CSI2_3PHASE_PIN8	CSI2_DP3	CSI2_1LANE_CLKP	CSI2_TRI2_B
CSI2_3PHASE_PIN9	CSI2_DN3	CSI2_1LANE_CLKN	CSI2_TRI2_C

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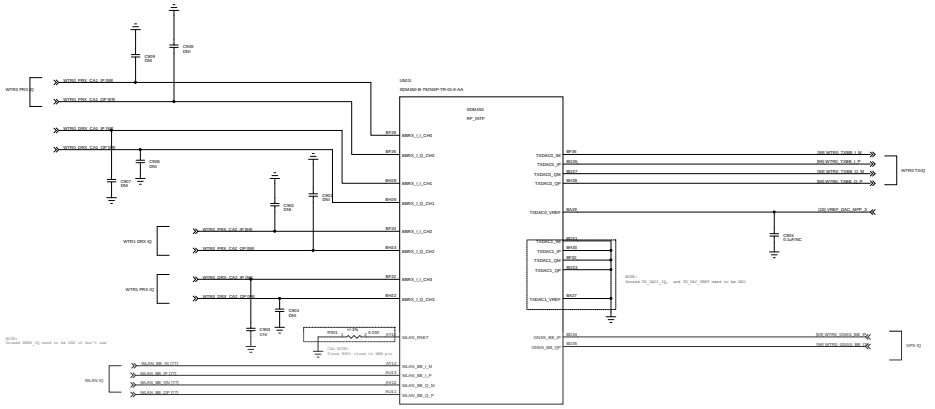

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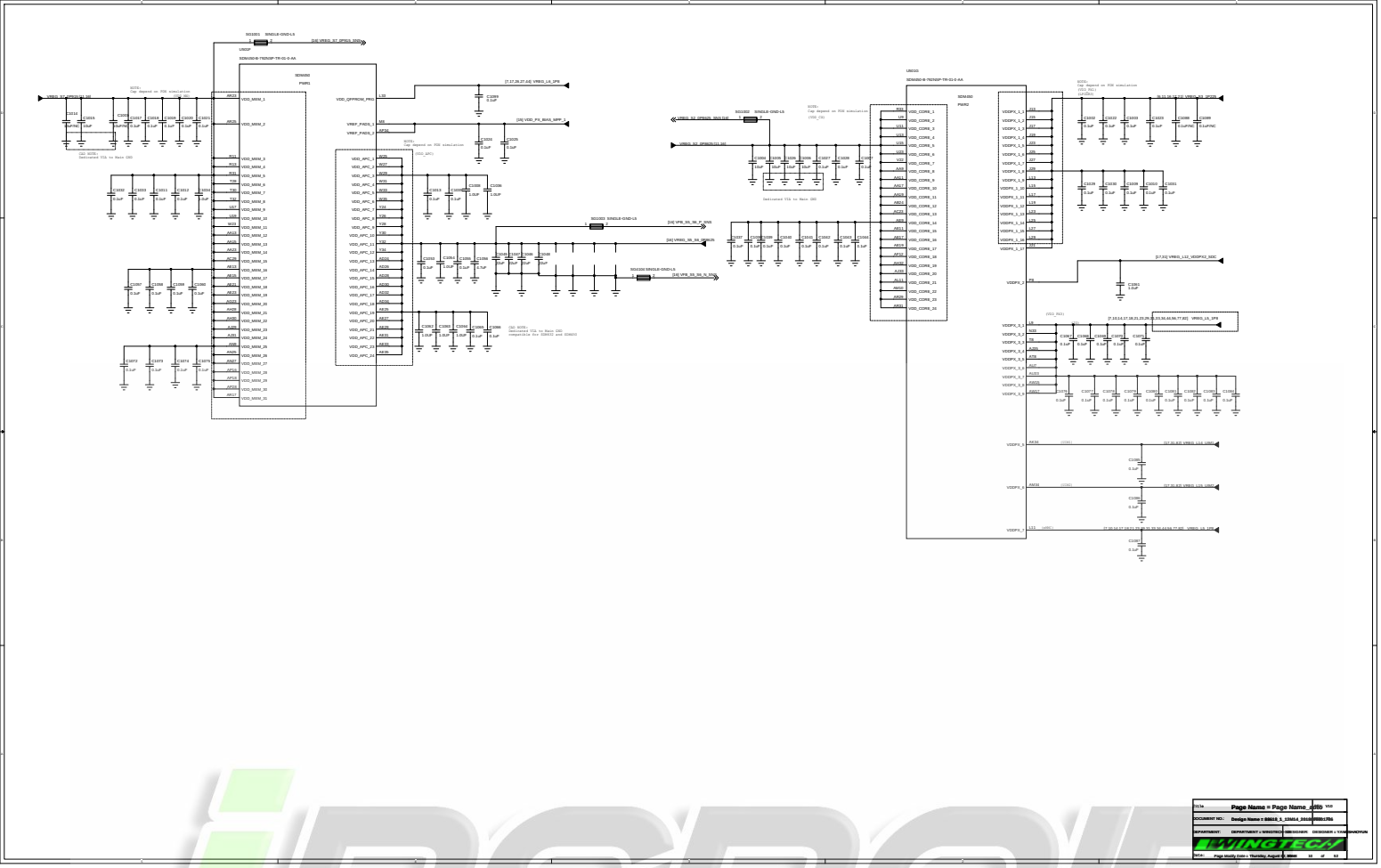

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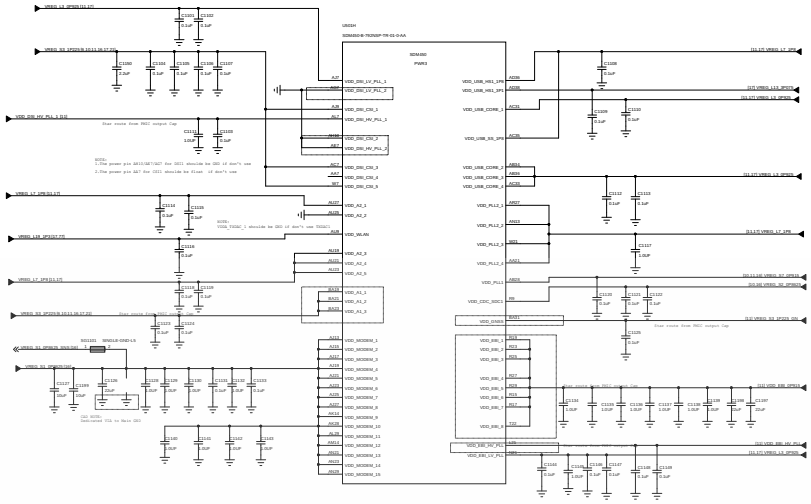

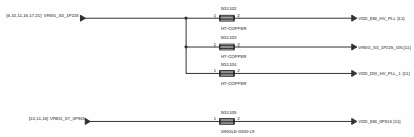

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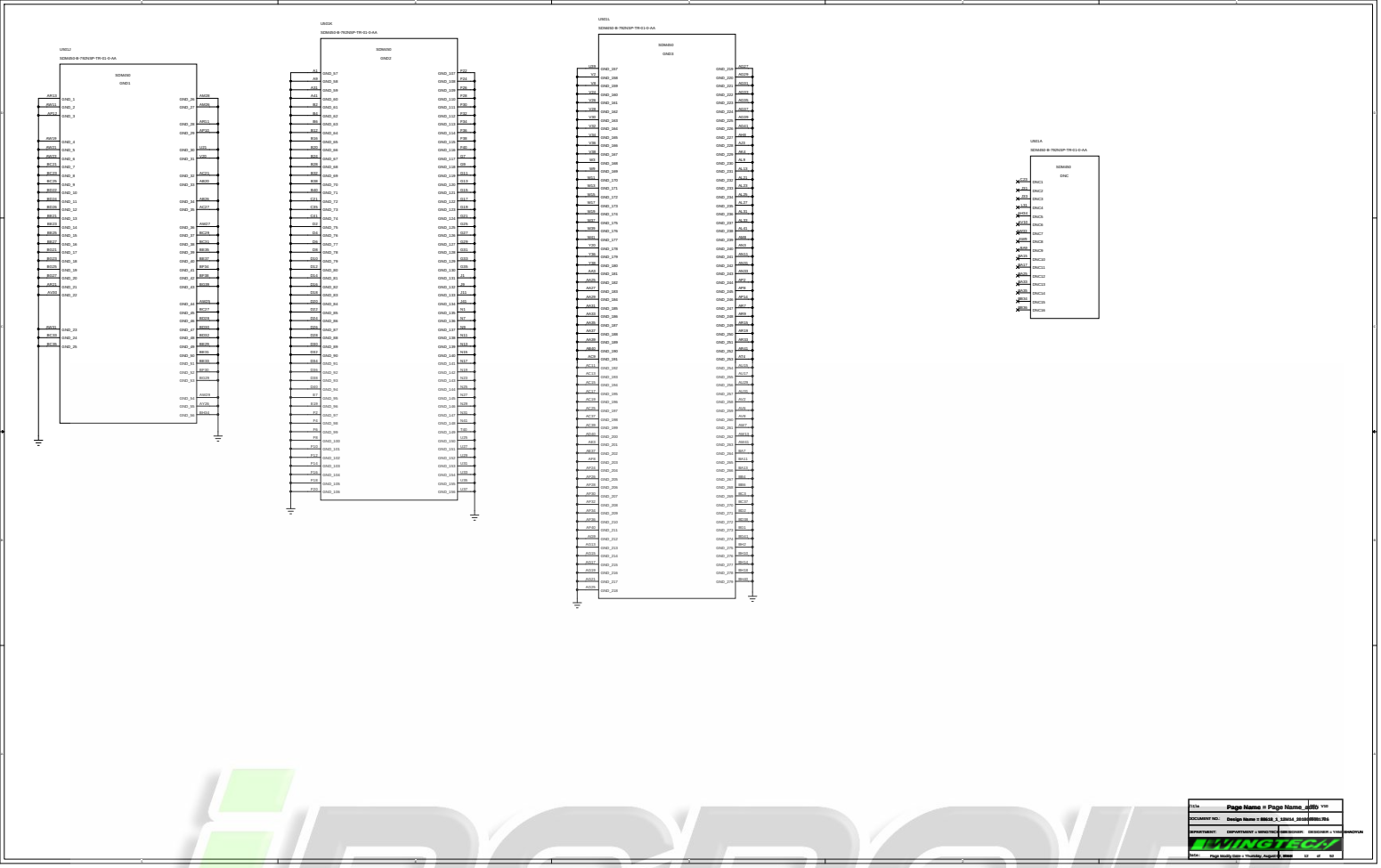

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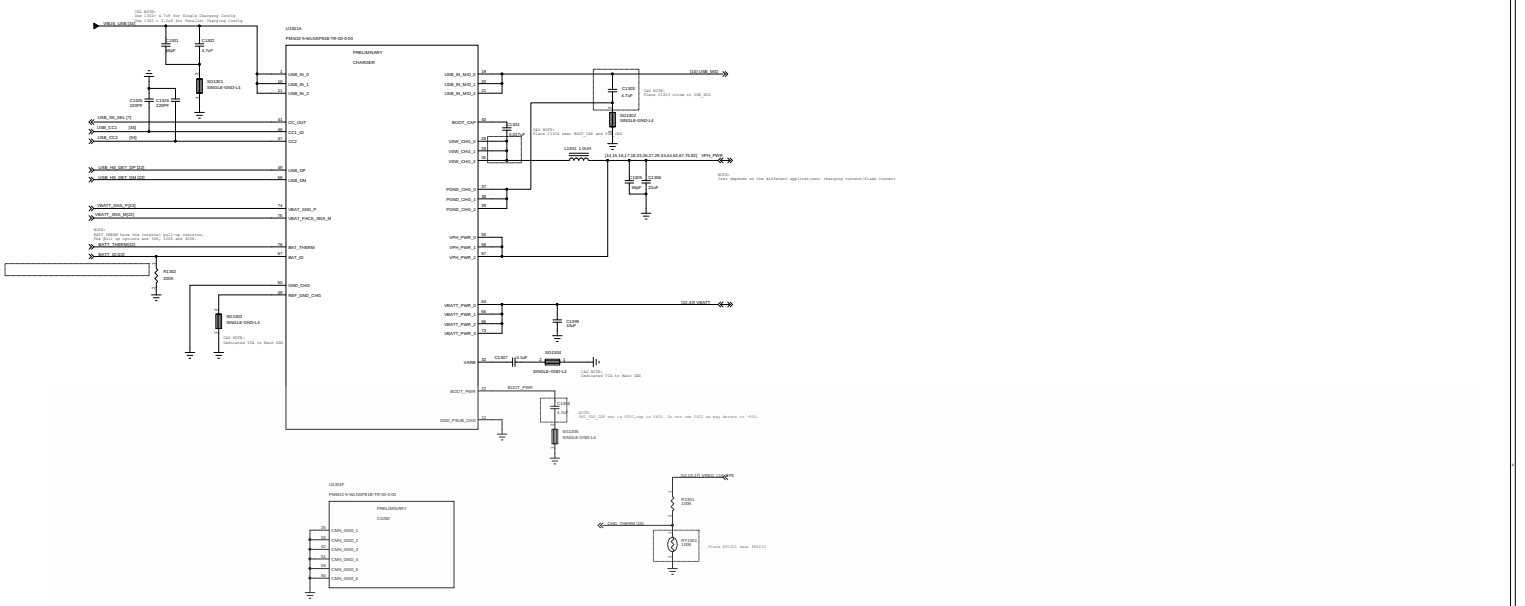

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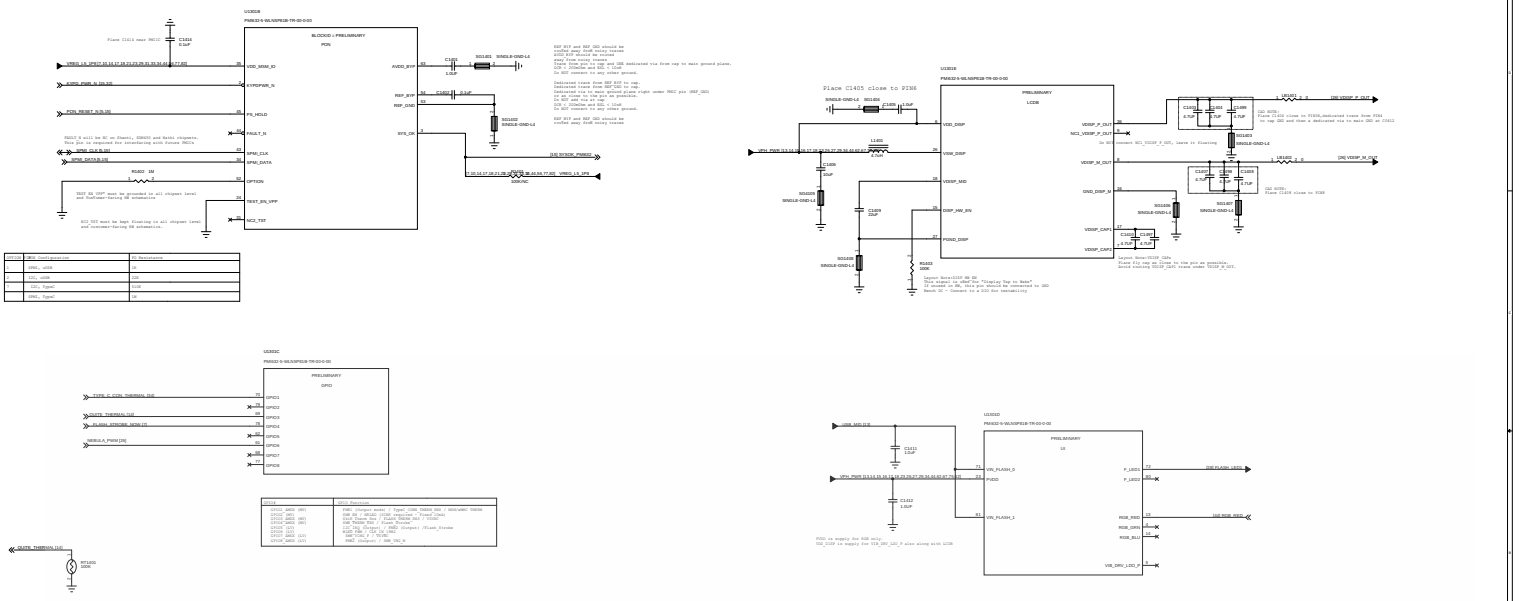

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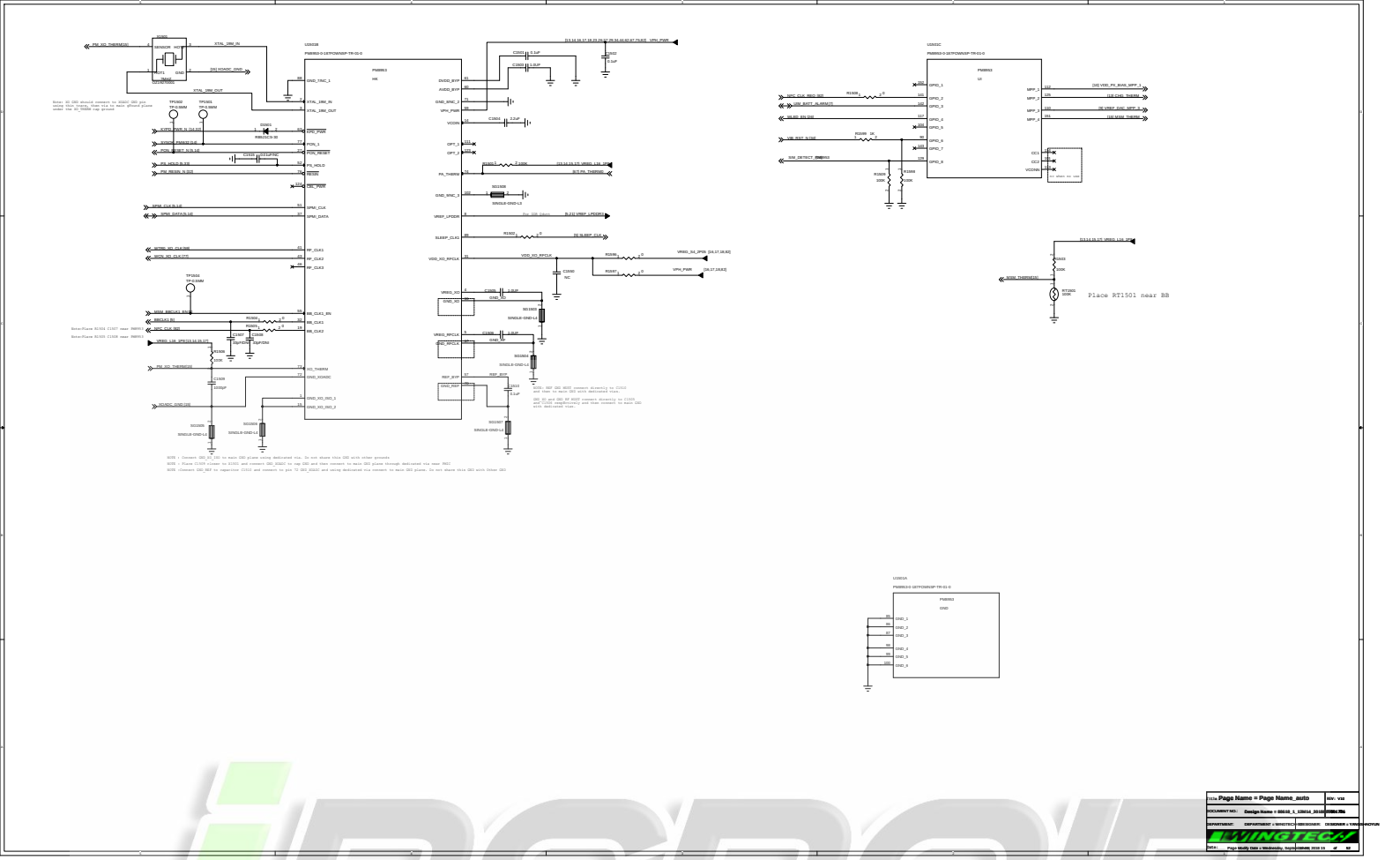

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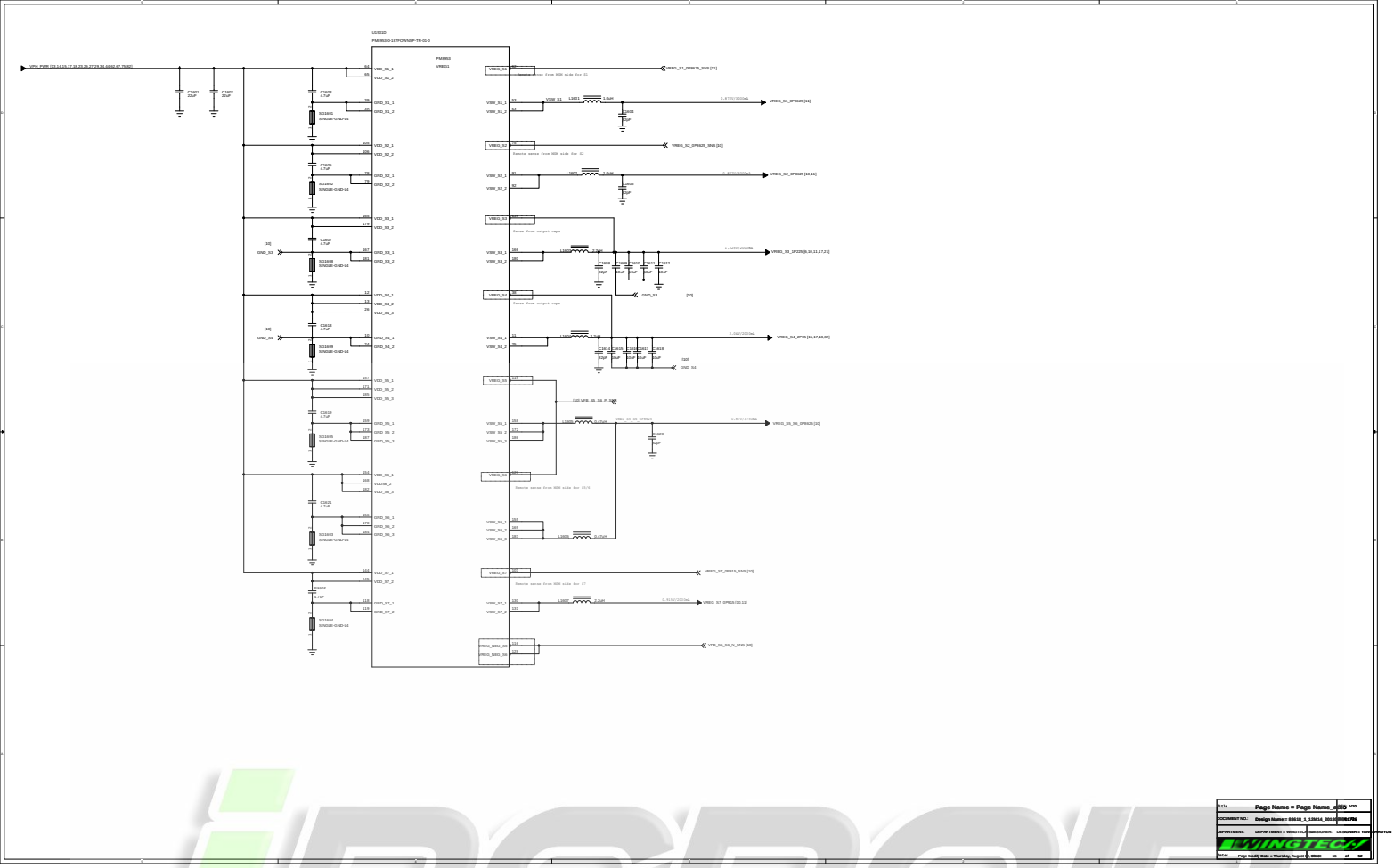

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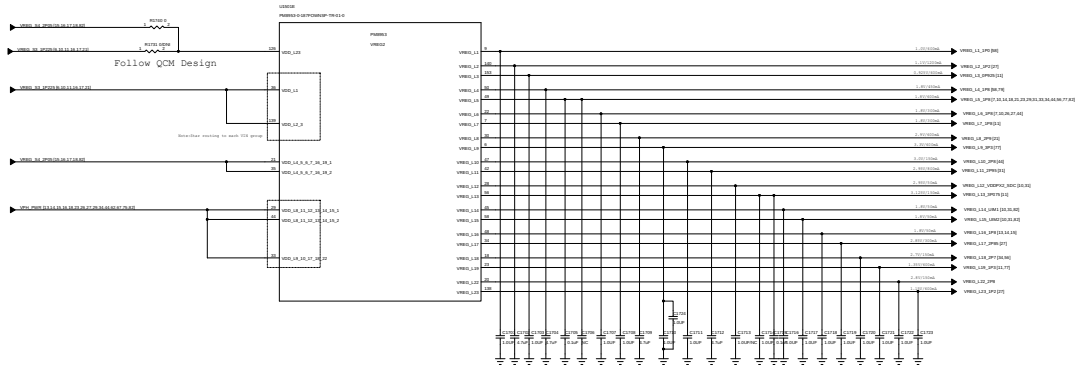

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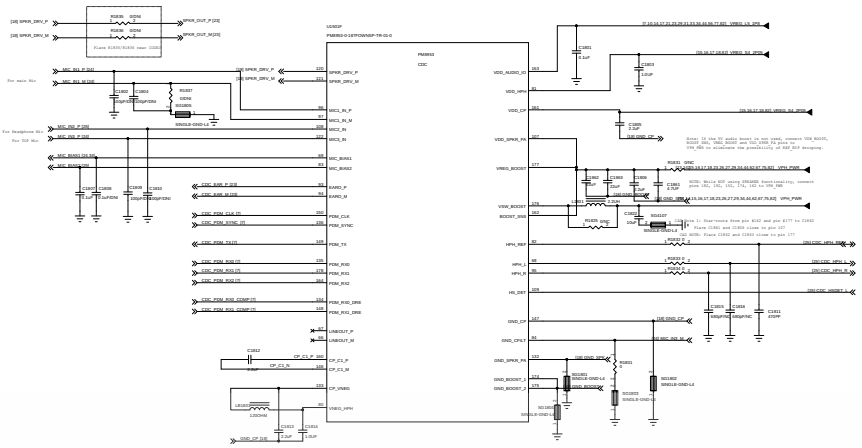

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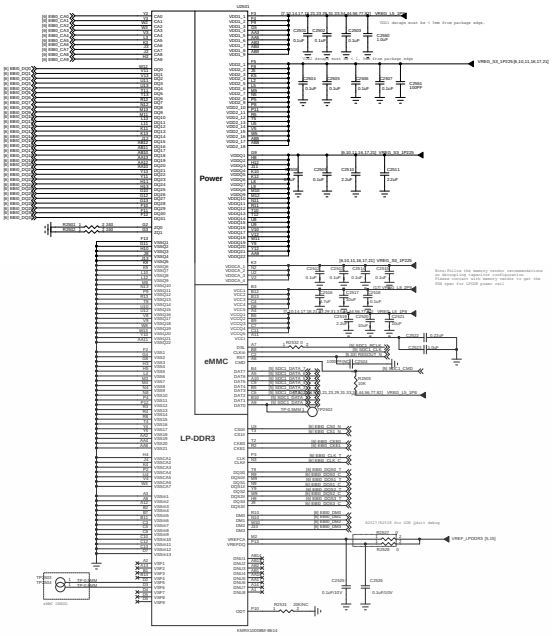

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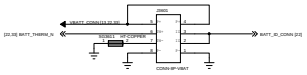

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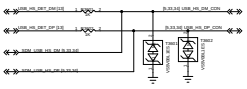

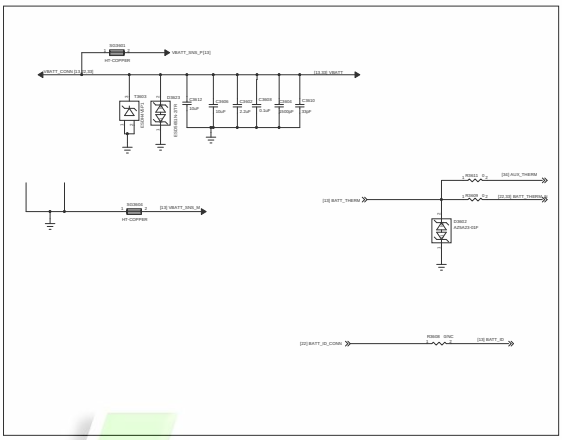

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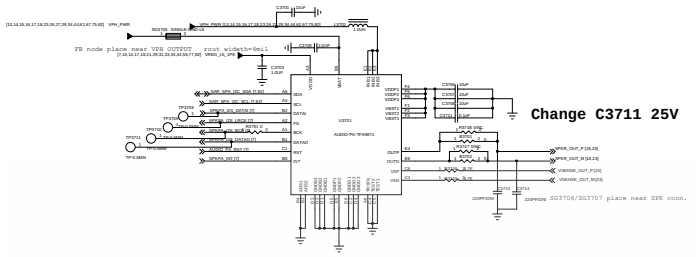
iREPAIR
LABORATORIO TÉCNICO
MICROSOLDADURA

AUDIO PA



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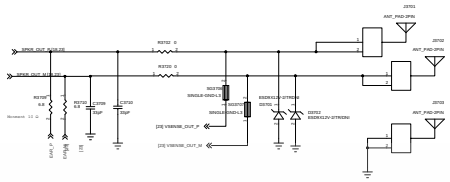

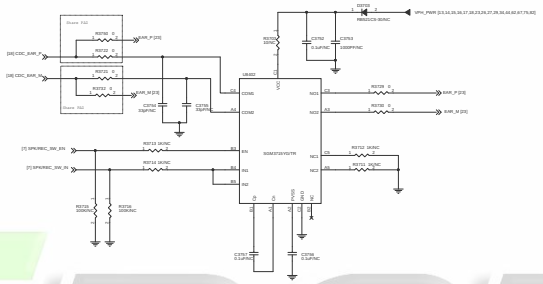

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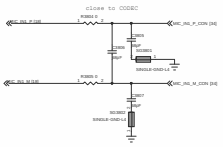
LABORATORIO TÉCNICO
MICROSOLDADURA

Mic2

Mic1



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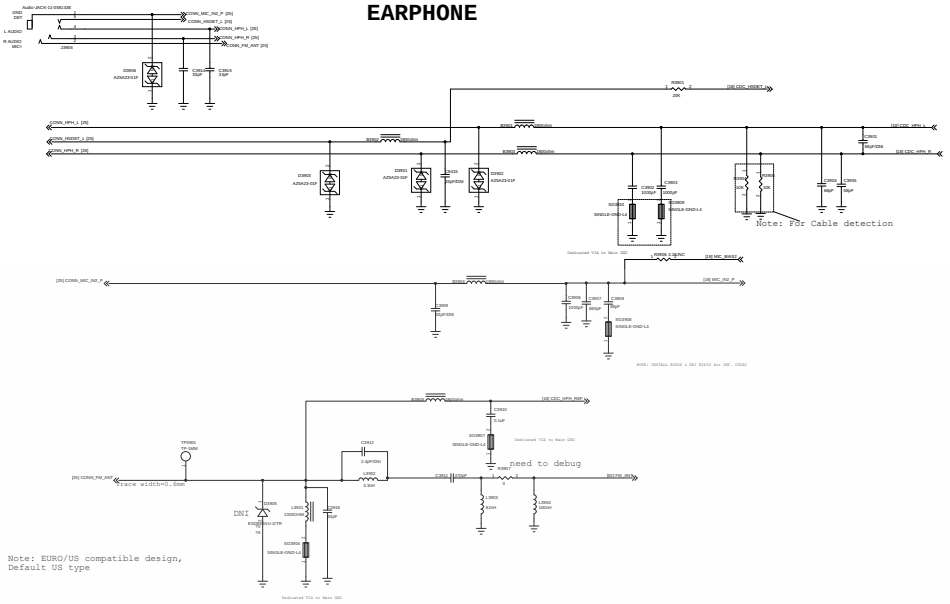
LABORATORIO TÉCNICO
MICROSOLDADURA

EARPHONE



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WINGTECH			
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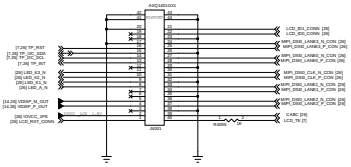
LCD-Connector



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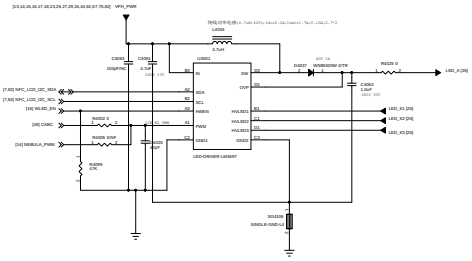

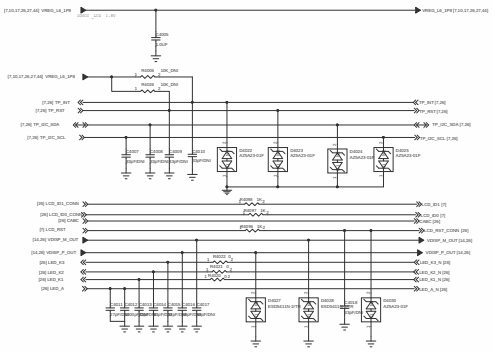

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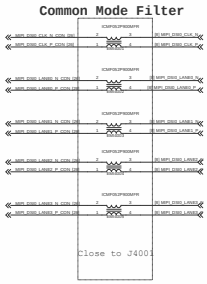

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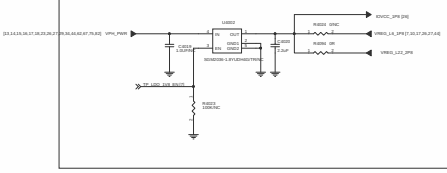

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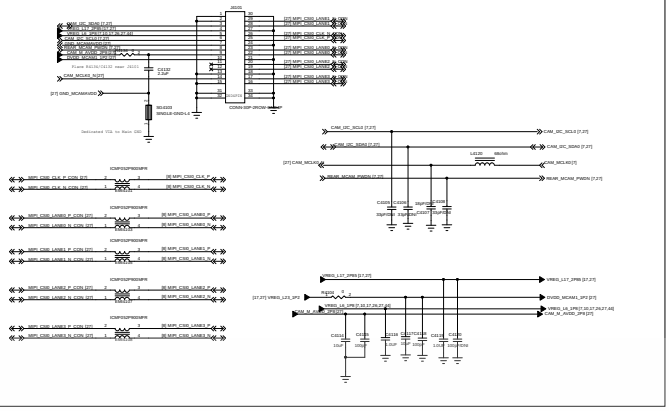
Main Camera

CAD note: Ensure the DGND to main-GND directly and robustly



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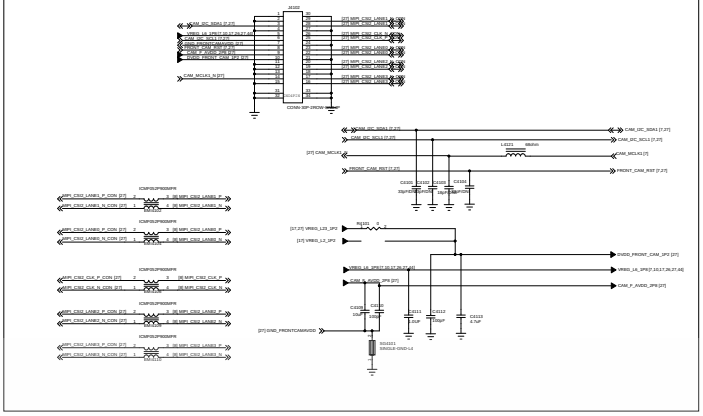

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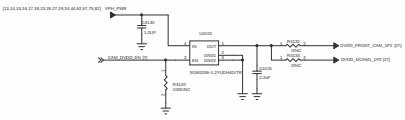

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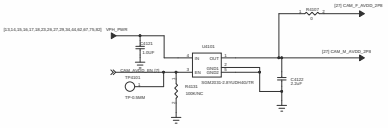

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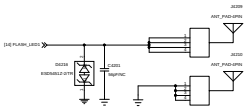
Flash



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WINGTECH

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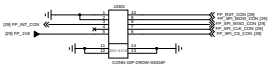
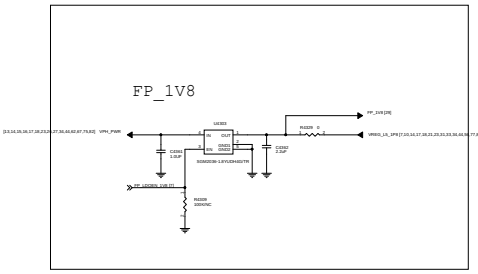
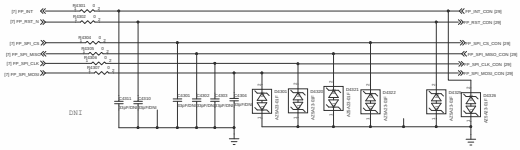
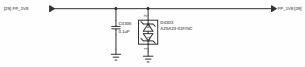
Fingerprint CONNECT









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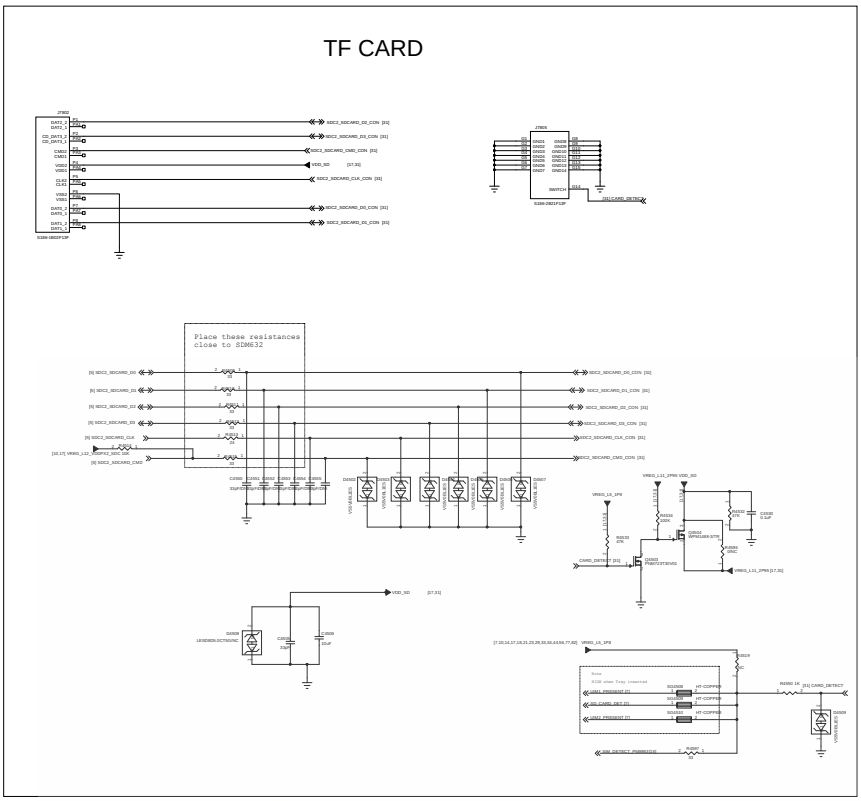

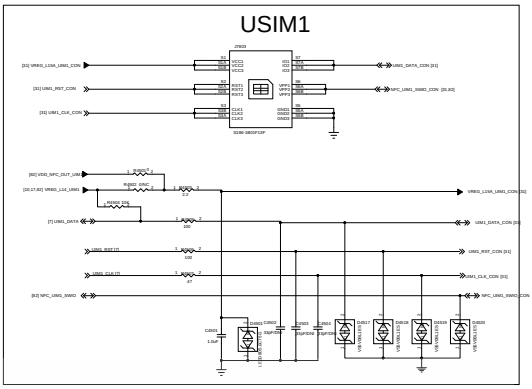

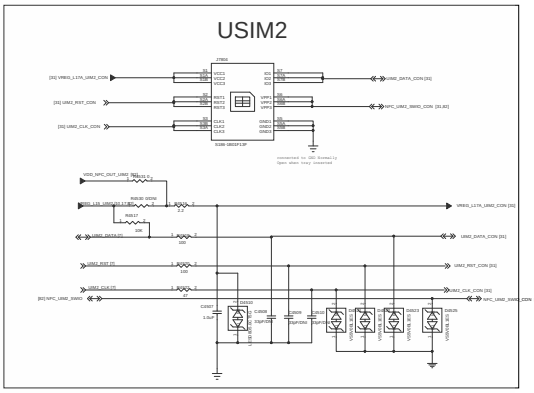

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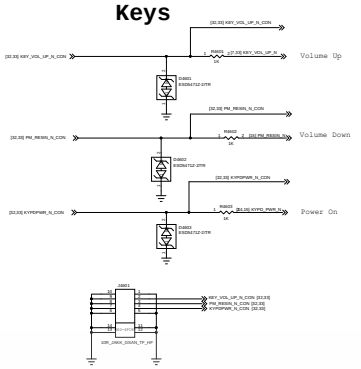
Keys



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WINGTECH

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LABORATORIO TÉCNICO
MICROSOLDADURA

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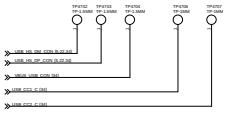

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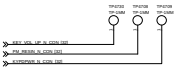

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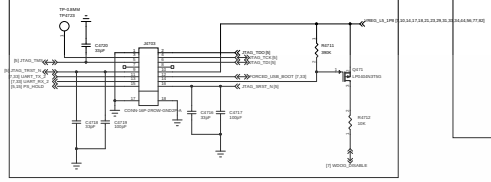

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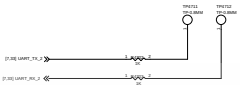

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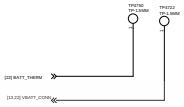

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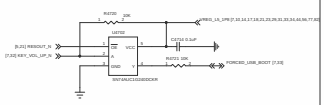

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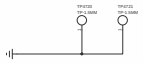

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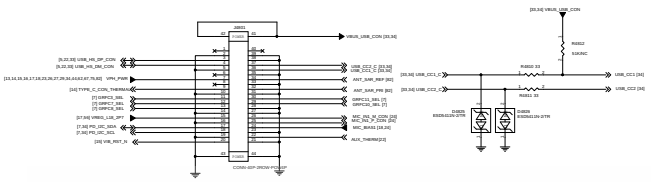
Sub Board Connector



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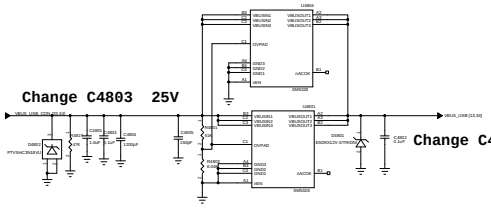

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LD20-PB825-20

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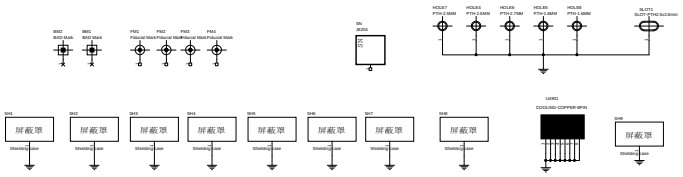

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WINGTECH

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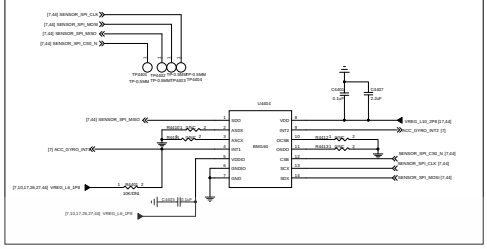
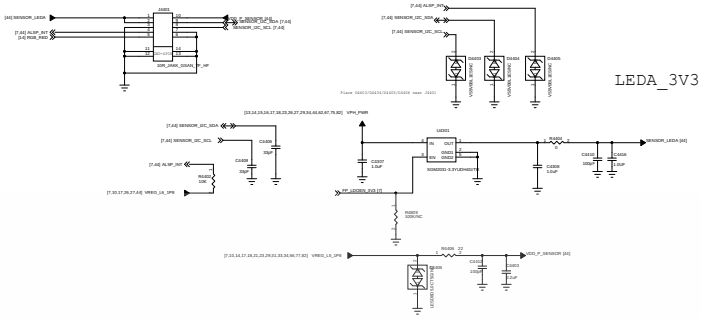
ACCELEROMETER+GYRO

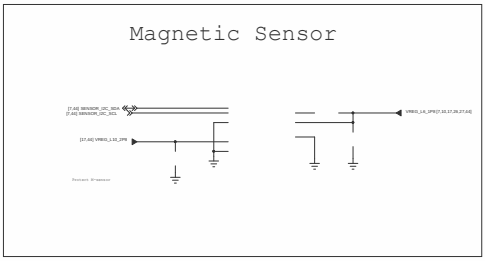


ALS/PROXIMITY SENSOR/LED indicator



LEDA_3V3

Magnetic Sensor



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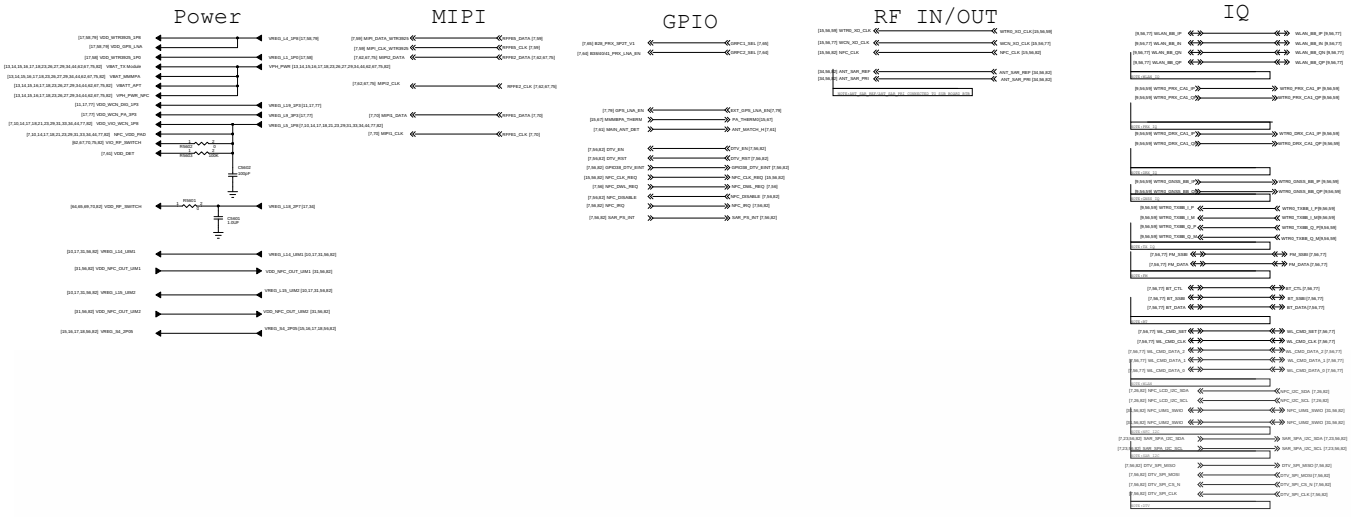

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DOCUMENT NO.: W9925TECH-SH-HW-SC4331	SIZE: D
DEPARTMENT: Shanghai Hardware	DESIGNER: WHPHHBQ
WINGTECH	
Date: 2019-03-28	Sheet of

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details refer to Cele RF Block Diagram.pdf

Title: SONGCO-WT9120S-ABUS CAT 6 移动摄像	REV: V10
DOCUMENT NO.: WR9120CH-SH-HW-SCH001	SIZE: D
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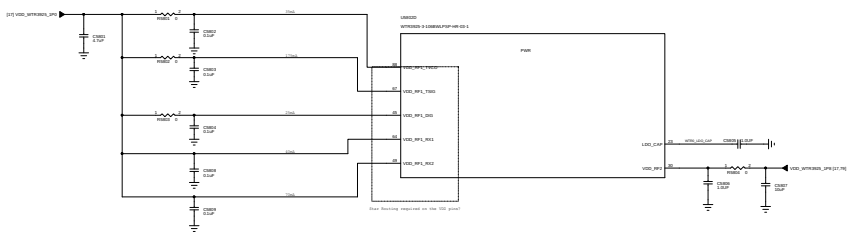

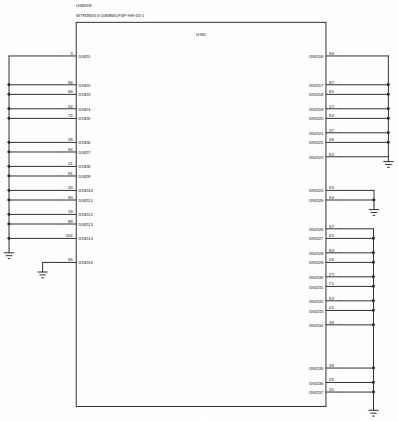

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DOCUMENT NO.: W960TECH-SH-HW-SCH001	SIZE: D
DEPARTMENT: Shanghai Hardware	DESIGNER: WHPHENQ
WINGTECH	
Date: 2018-03-09	Sheet of

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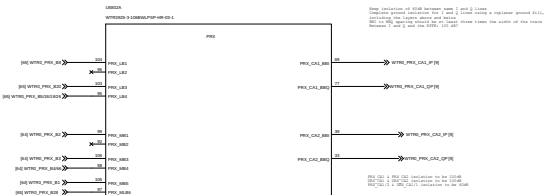
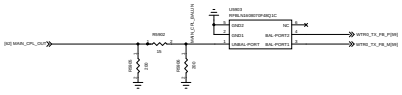
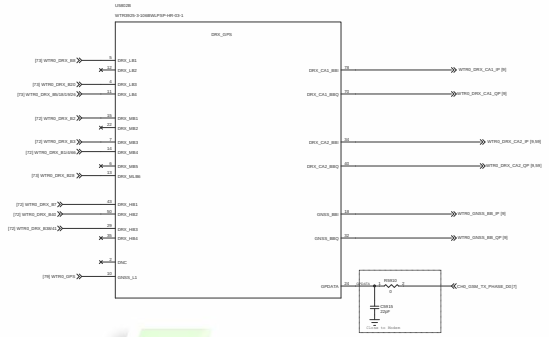
MICROSOLDADURA

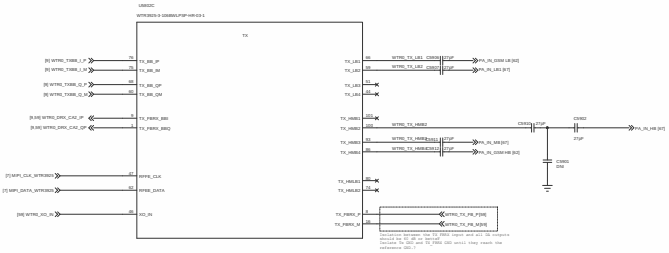


Keep isolation of 50dB between same I and Q lines
Complete ground isolation for I and Q lines using a coplanar ground fill, including the layers above and below
Keep the ground fill spacing to the trace three times the width of the trace
Between I and Q and the RFFE: [10 dB]

PRX CA1 & PRX CA2 isolation to be 10dB
DRX CA1 & DRX CA2 isolation to be 10dB
PRX CA1/2 & DRX CA1/2 isolation to be 50dB







Keep the DRX and TX FBRX CA2 control they reach the reference [9]

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DEPARTMENT: Shanghai Hardware	DESIGNER: WHP-HHQ
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Date: 2018-03-28	Sheet of

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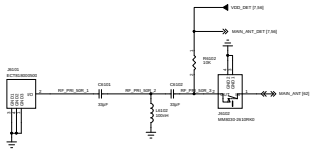

Title: SDM660+WTR3930 sub1 CAT 台湾感测	REV: V10
DOCUMENT NO.: W8K0TECH SH HW-SCH001	SIZE: D
DEPARTMENT: Shanghai Hardware	DESIGNER: WHPHHQ
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Date: 2018-03-09	Sheet of

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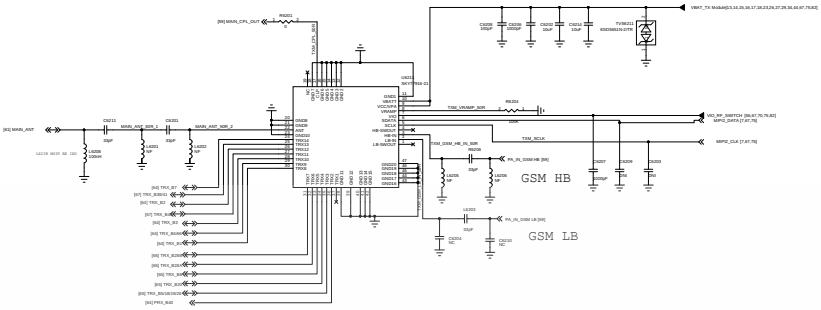

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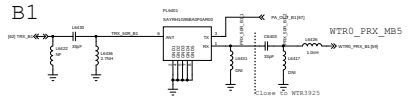

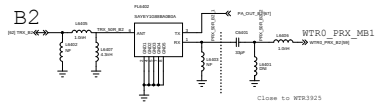

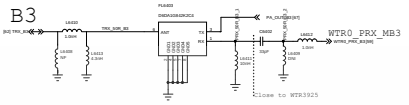

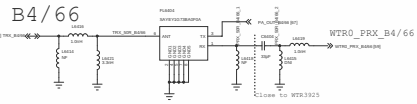

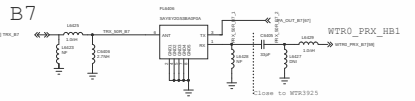

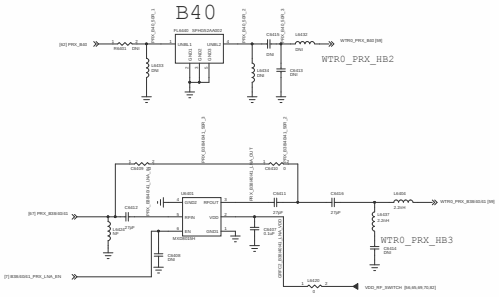

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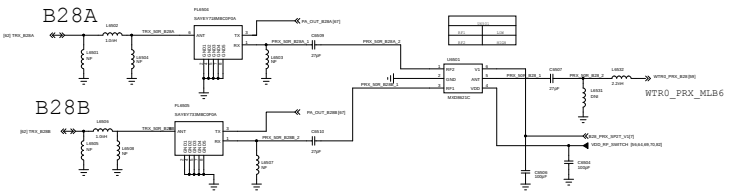

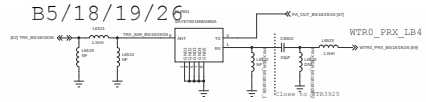

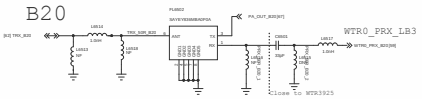

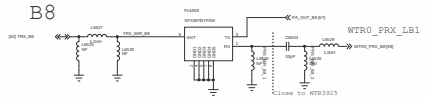

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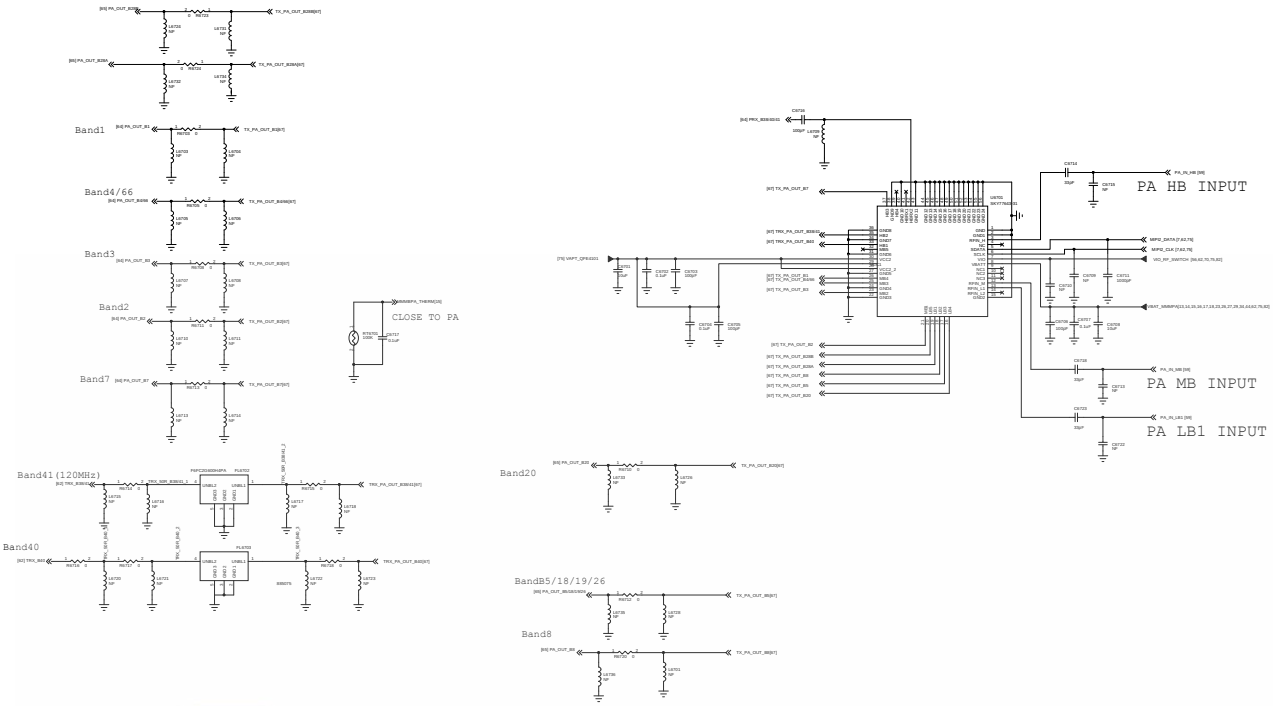

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DEPARTMENT: Shanghai Hardware	DESIGNER: WHPWHQ
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Date: 2018-02-28	Sheet of

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Title:	SONGDO-W7P120Ib adult CAT 台秤成图	REV: V02
DOCUMENT NO.:	W7P120Ib-SH-HW-SCH001	SIZE: D
DEPARTMENT:	Shanghai Hardware	DESIGNER: WUPANQ
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LABORATORIO TÉCNICO
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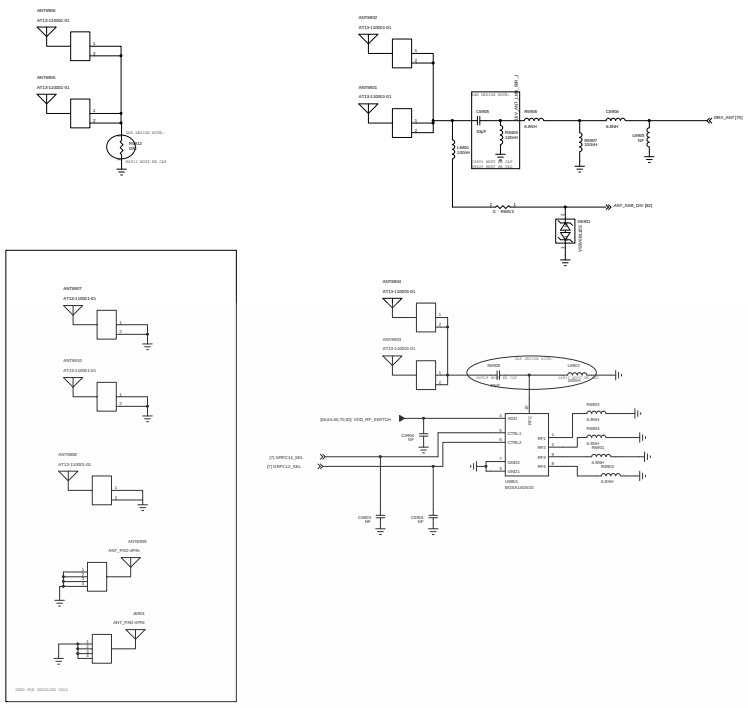
RF Port

Power



Title: SDM660+WTR3925 sub1 CAT 分集模组	REV: V10
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Date: 2018-03-28	Sheet of

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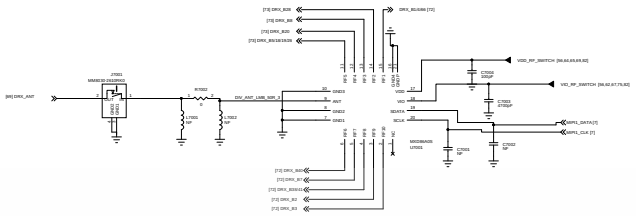

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Date:	Page Modify Date = Wednesday, Sept	2018

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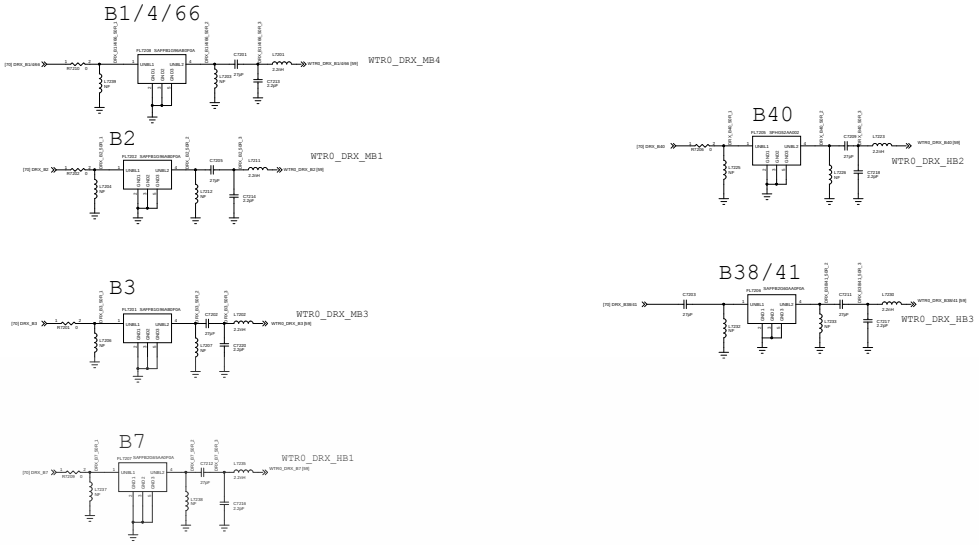

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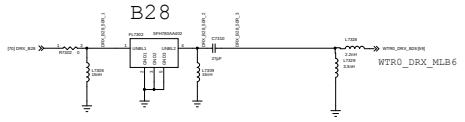

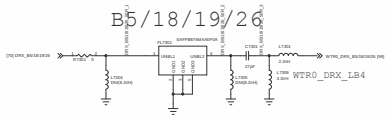

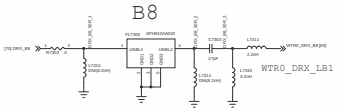

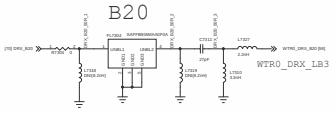

Title: SDM660+WTR3925 uMoS CAT 分频模组	REV: V10
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DEPARTMENT: Shanghai Hardware	DESIGNER: WHPHHQ
LIANGTECH	
Date: 2018-03-28	Sheet of

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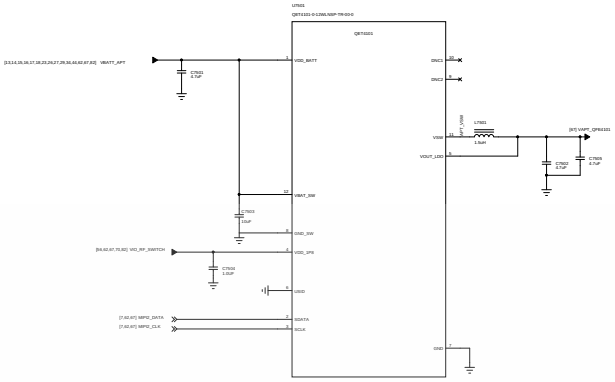
MICROSOLDADURA

RF Port



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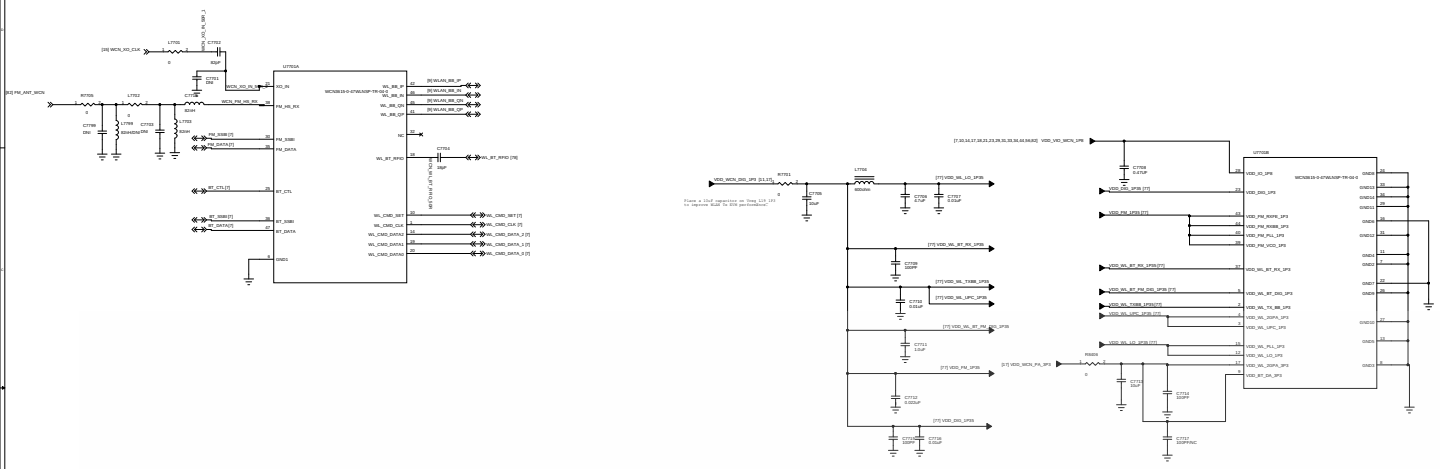

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77	of 86

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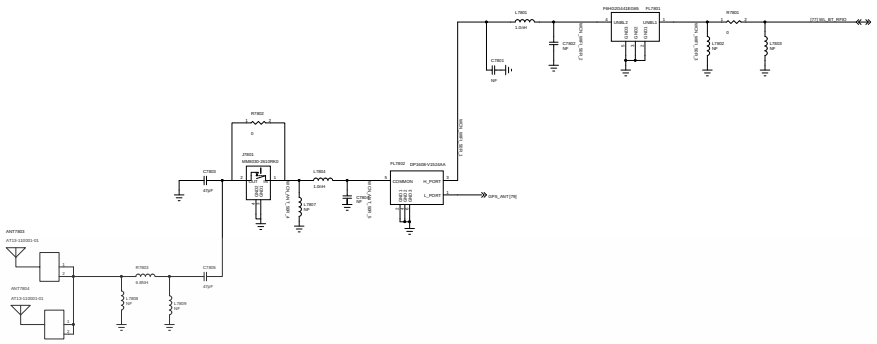

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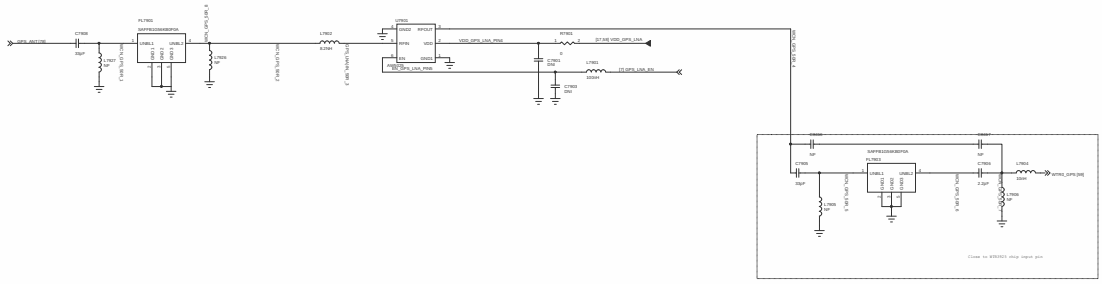

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DEPARTMENT	Shanghai Hardware	DESIGNER: WHP+WRQ
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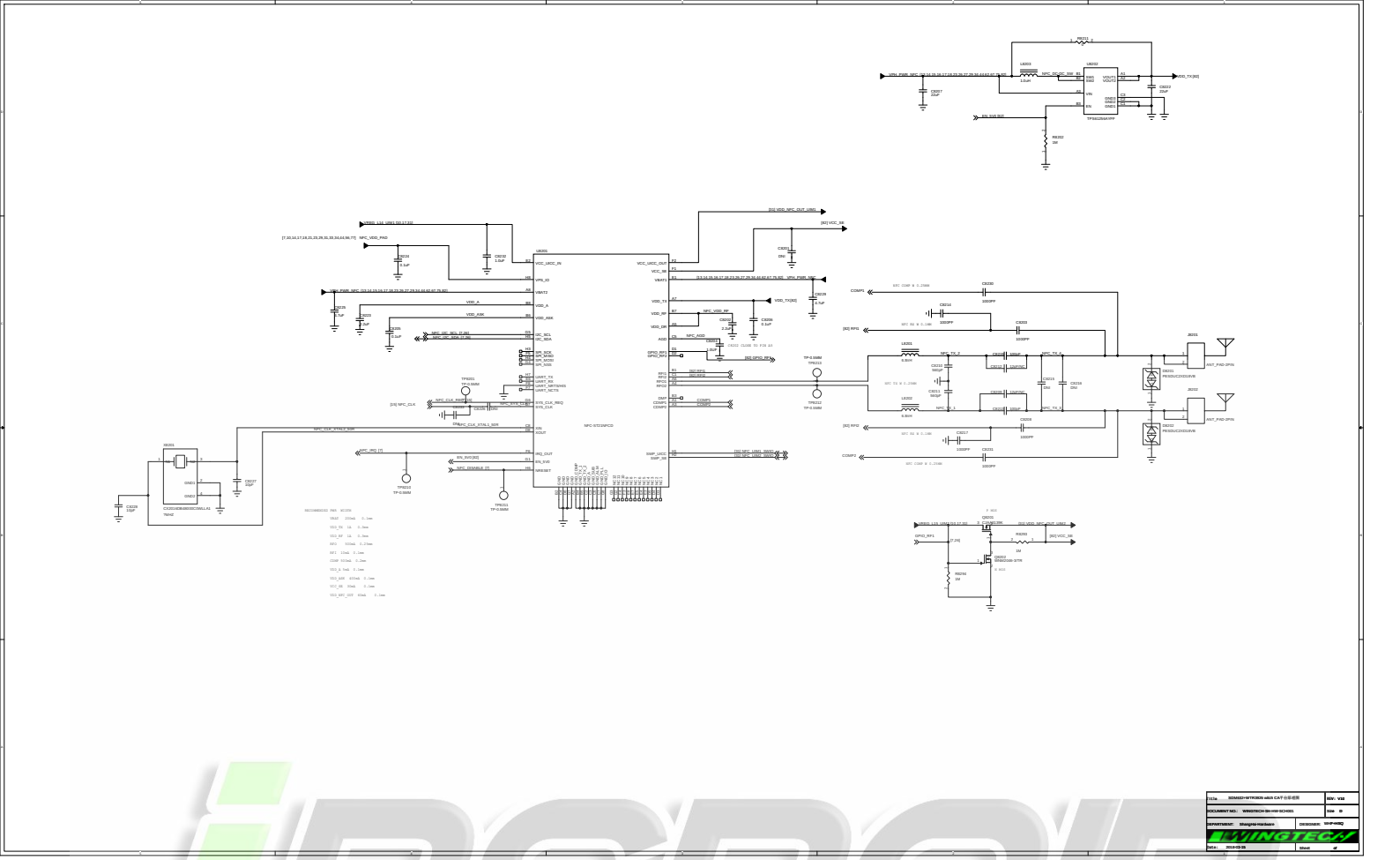
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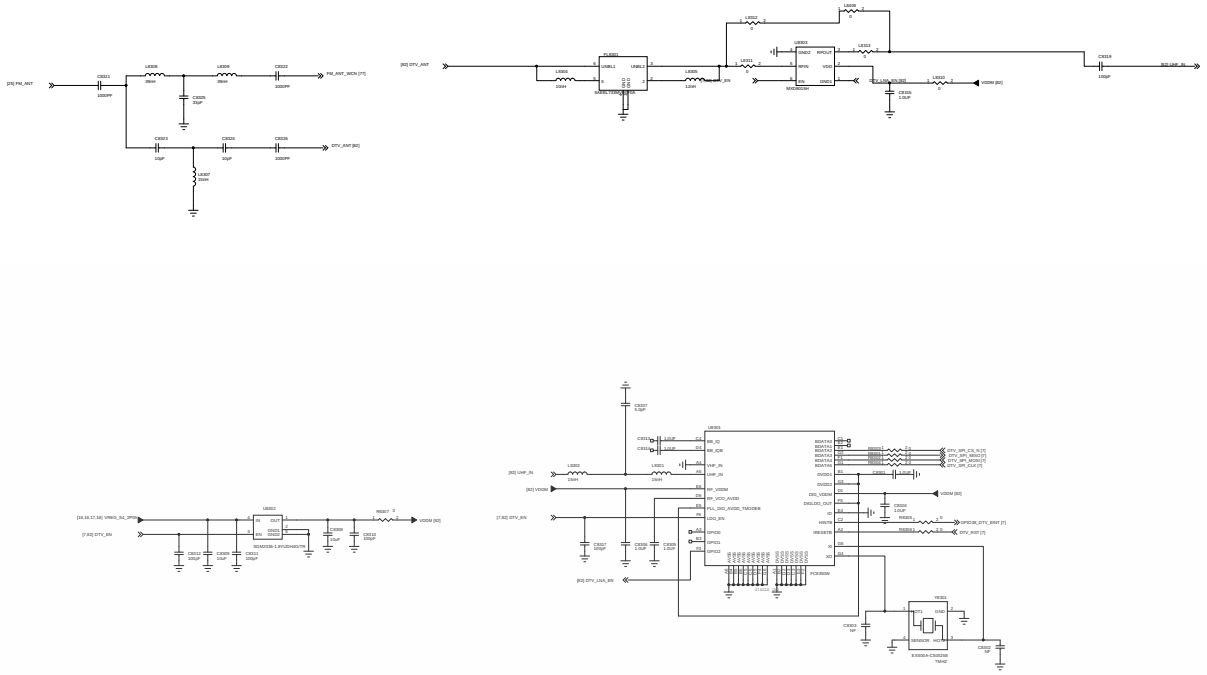

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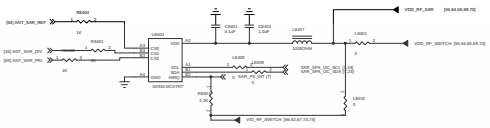

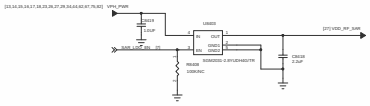

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